

FIG. 1

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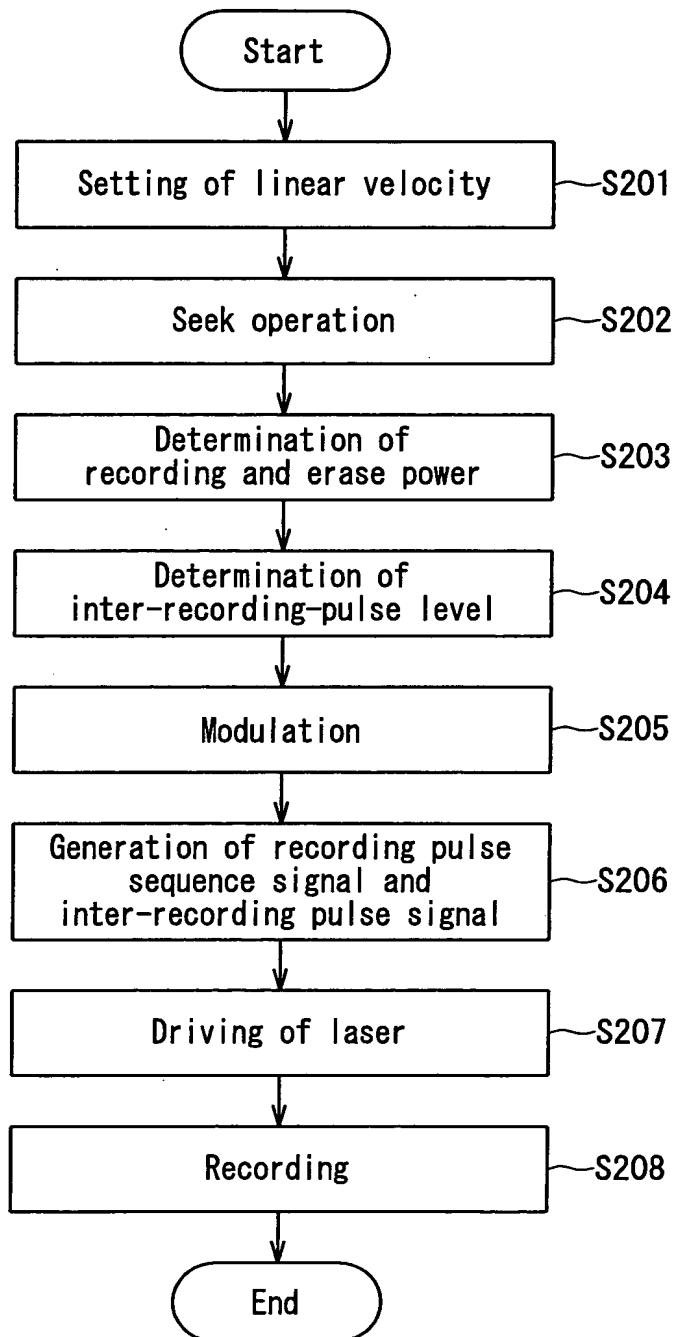


FIG. 2

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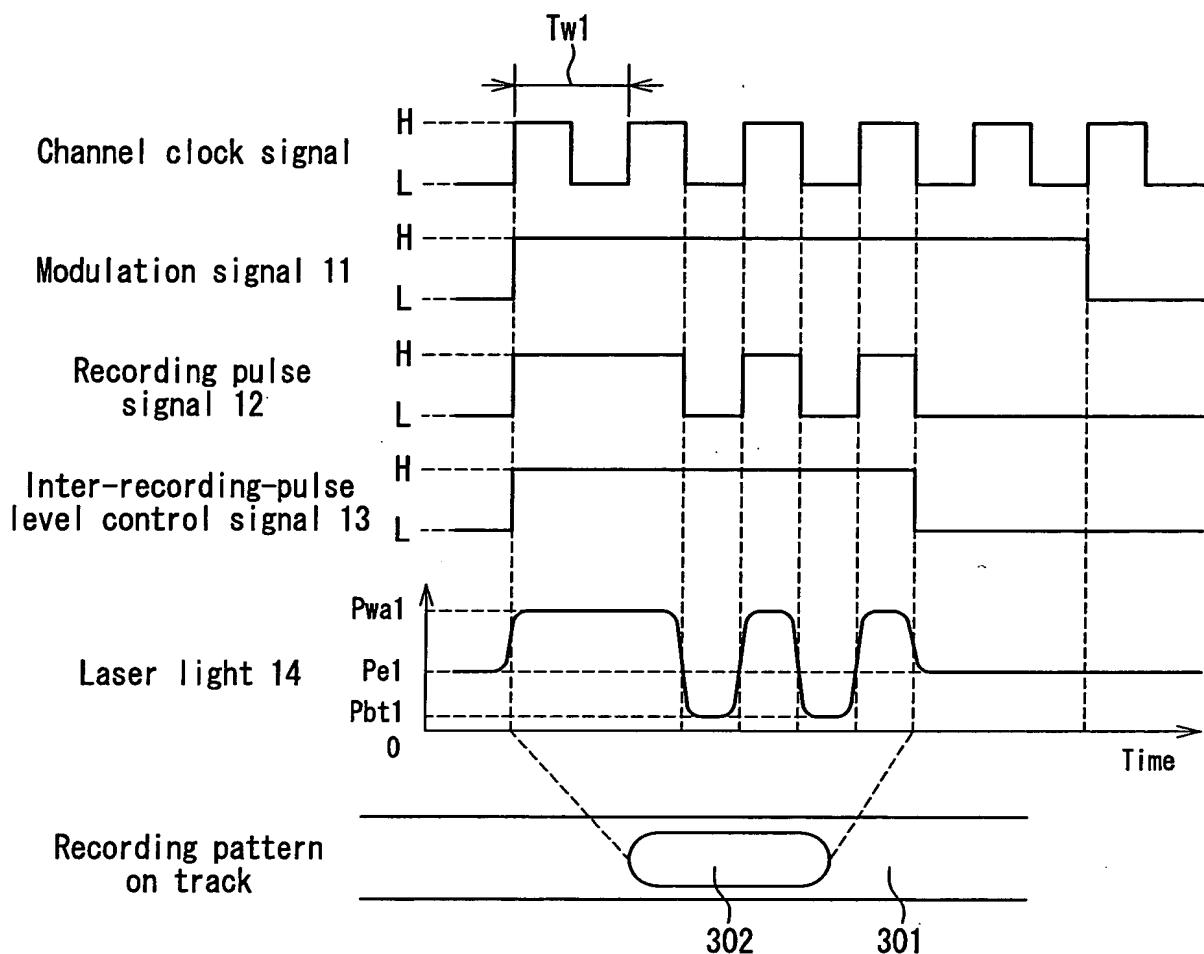


FIG. 3

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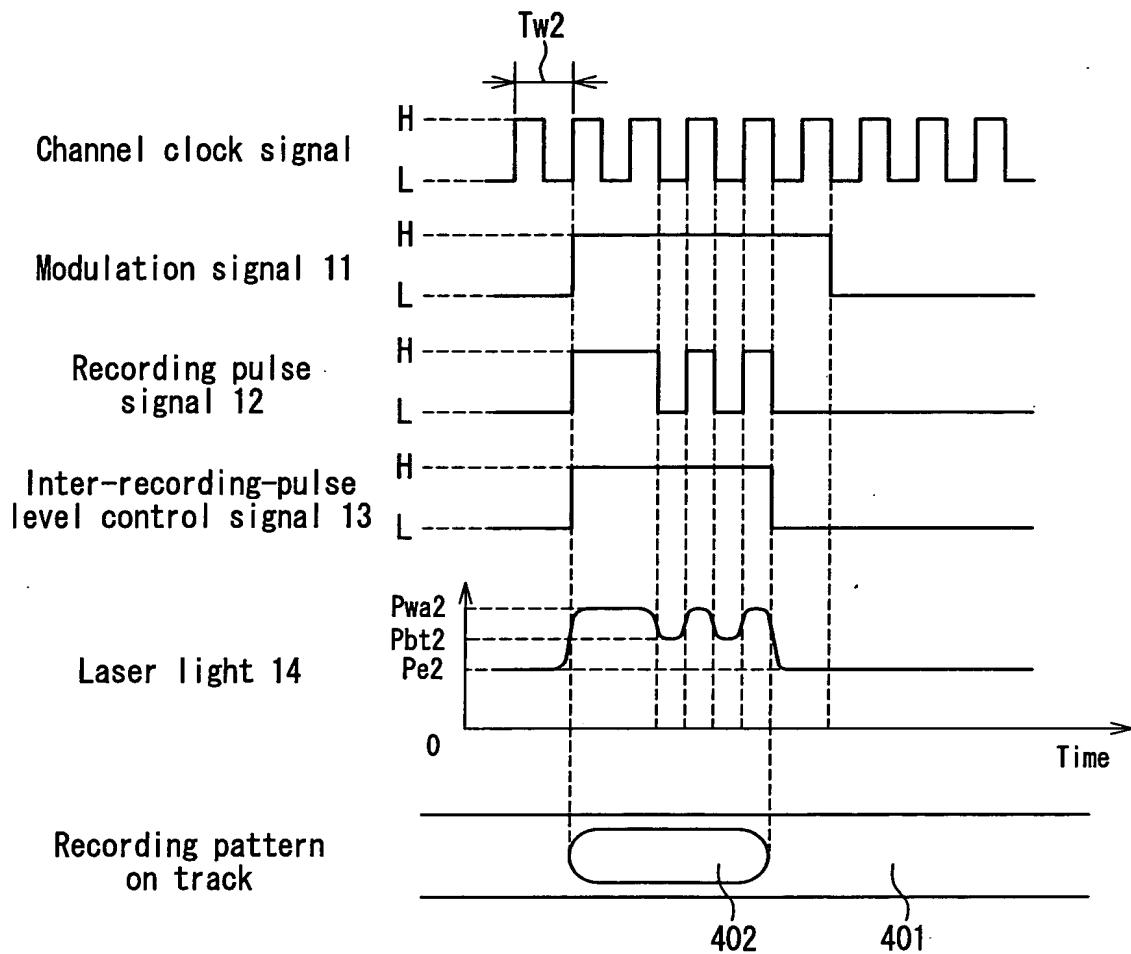


FIG. 4

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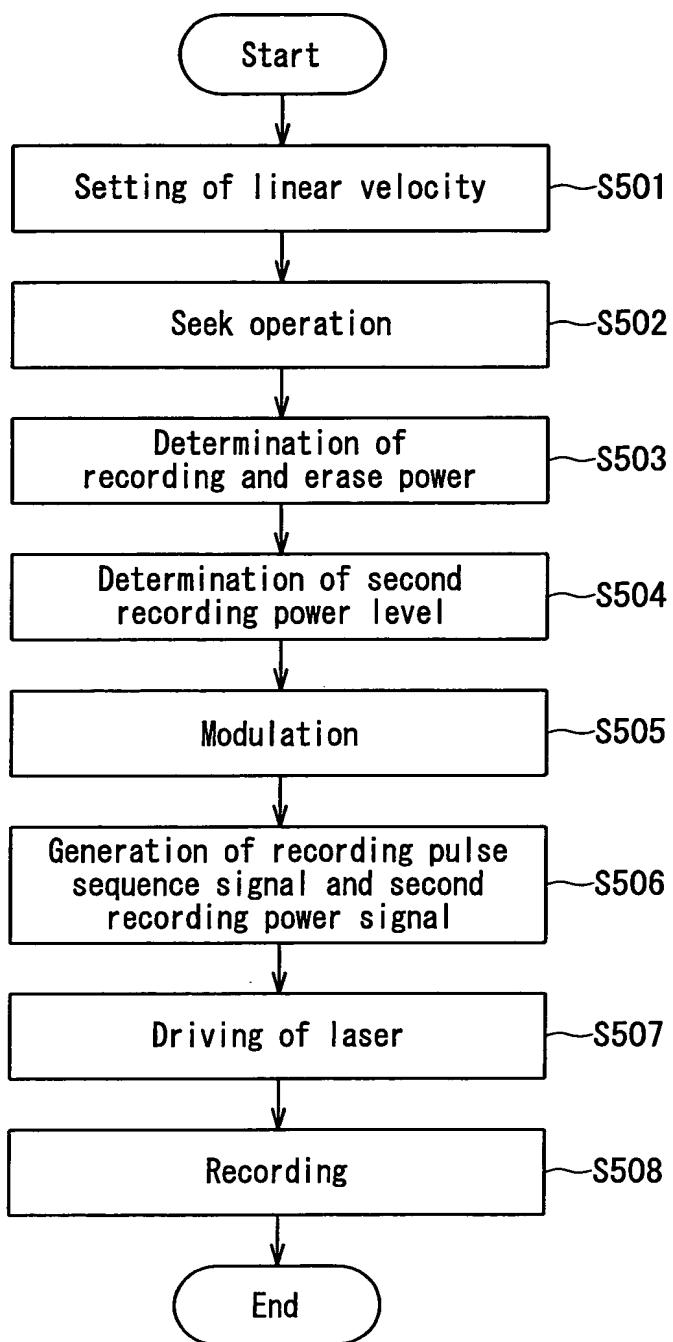


FIG. 5

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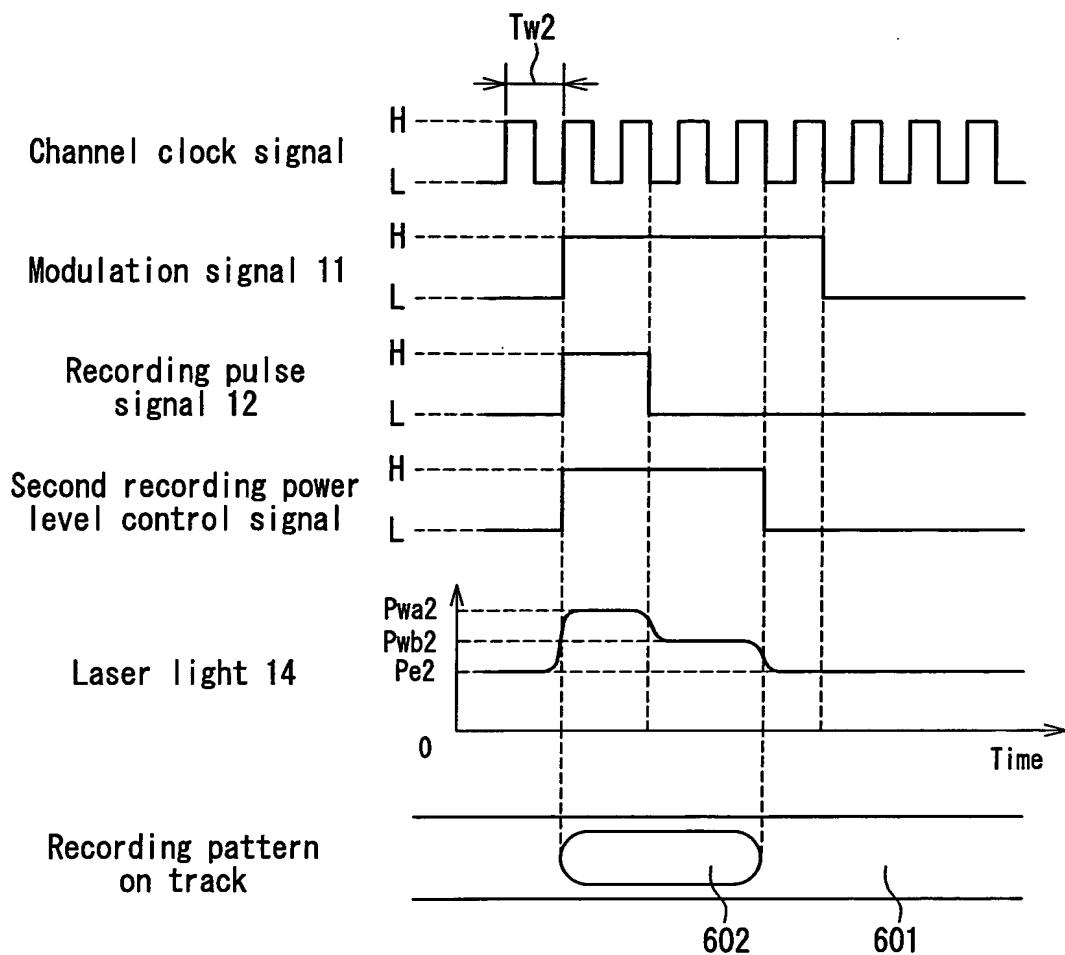


FIG. 6

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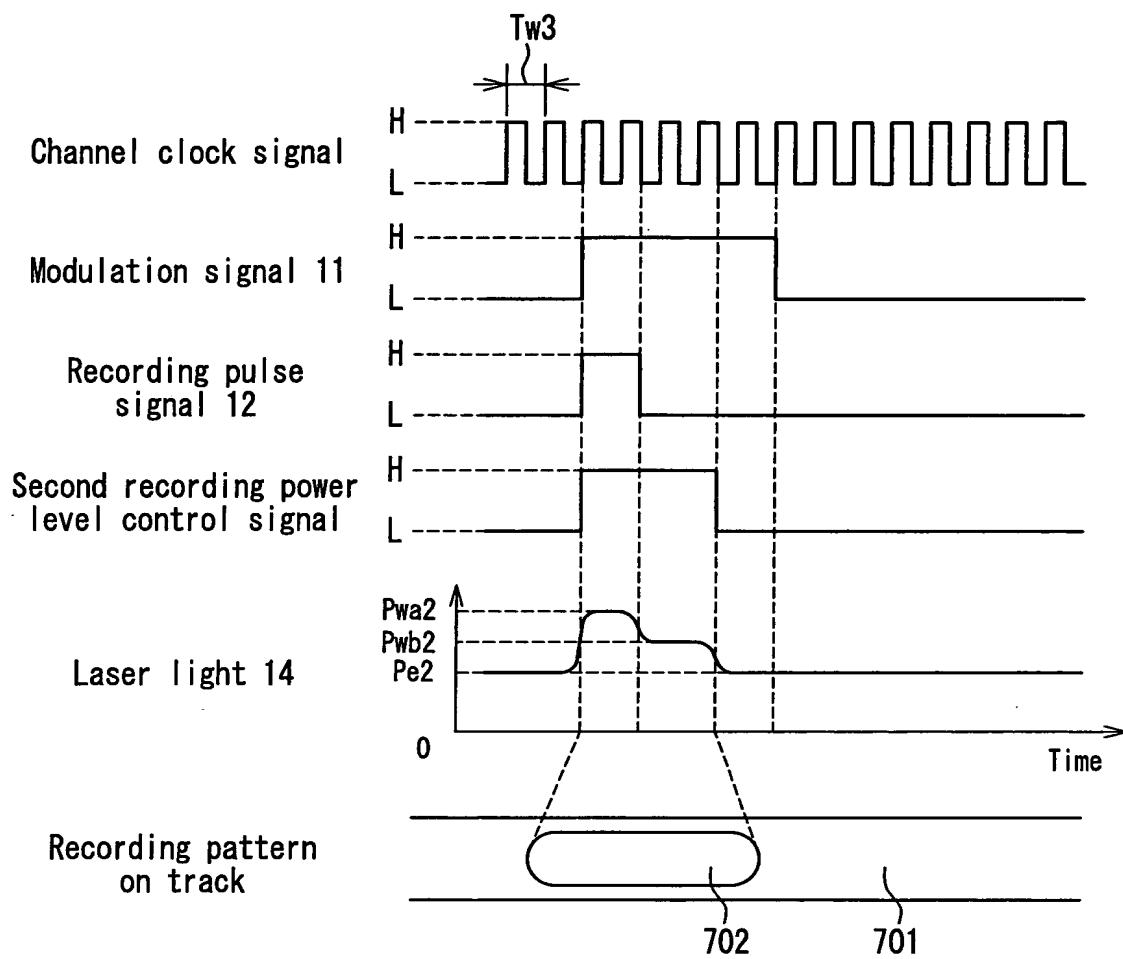


FIG. 7

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Inter-recording-pulse
power level

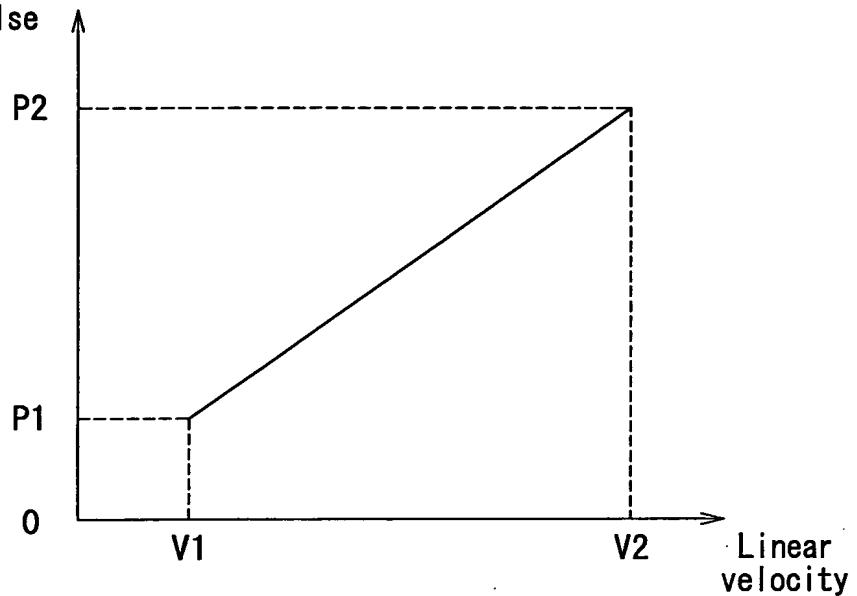


FIG. 8A

FIG. 8B

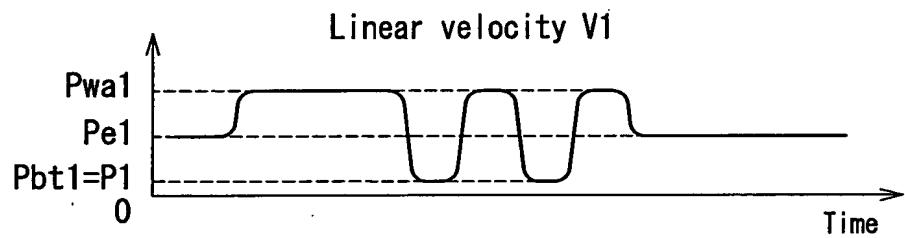
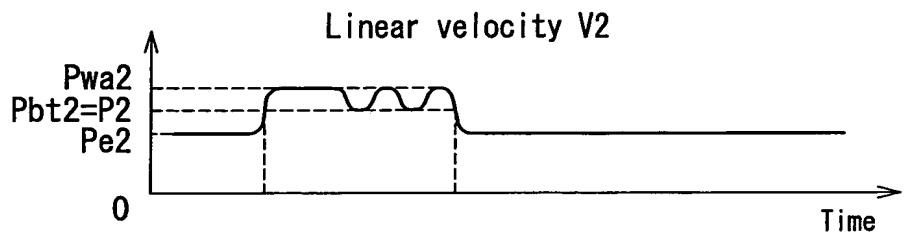
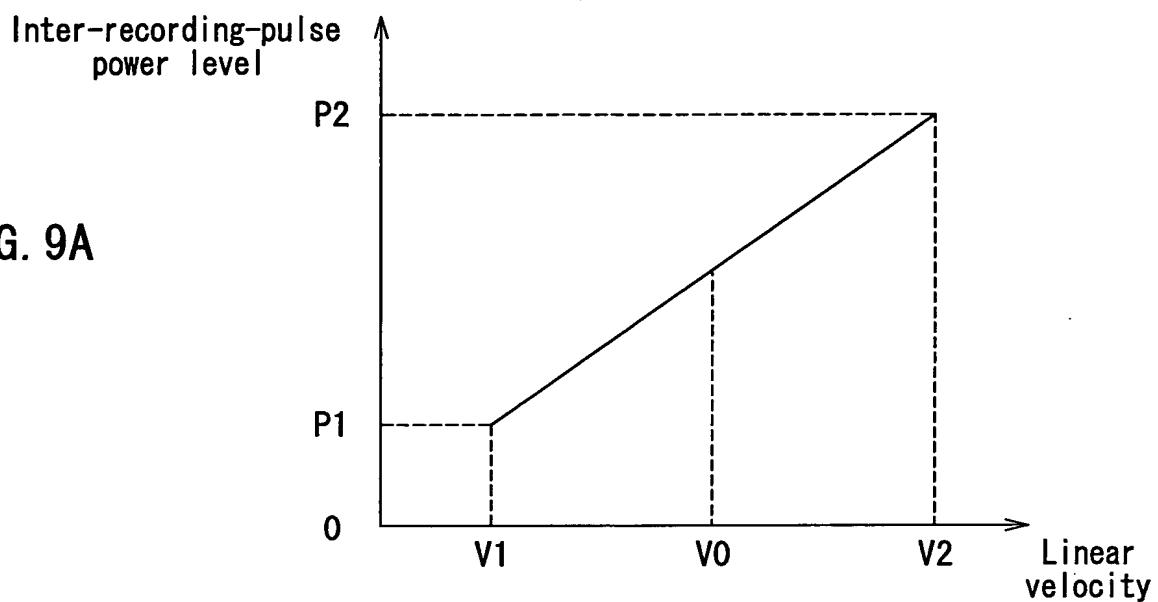
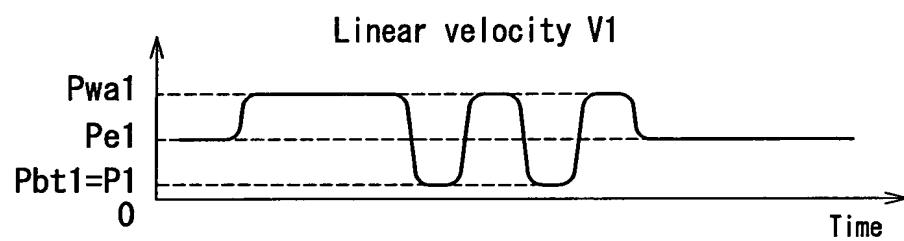
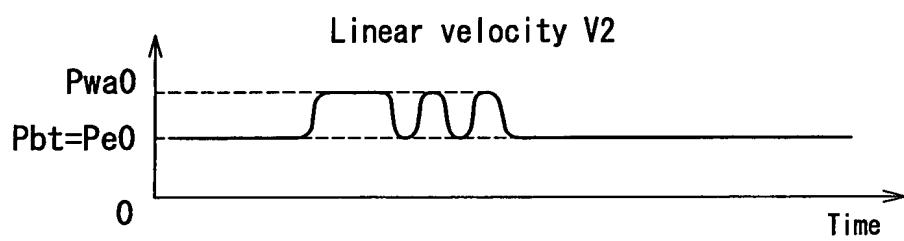
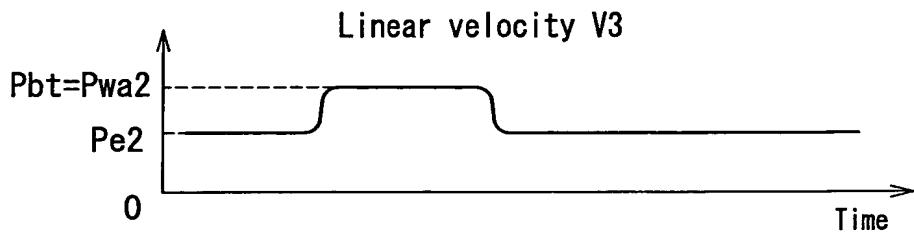


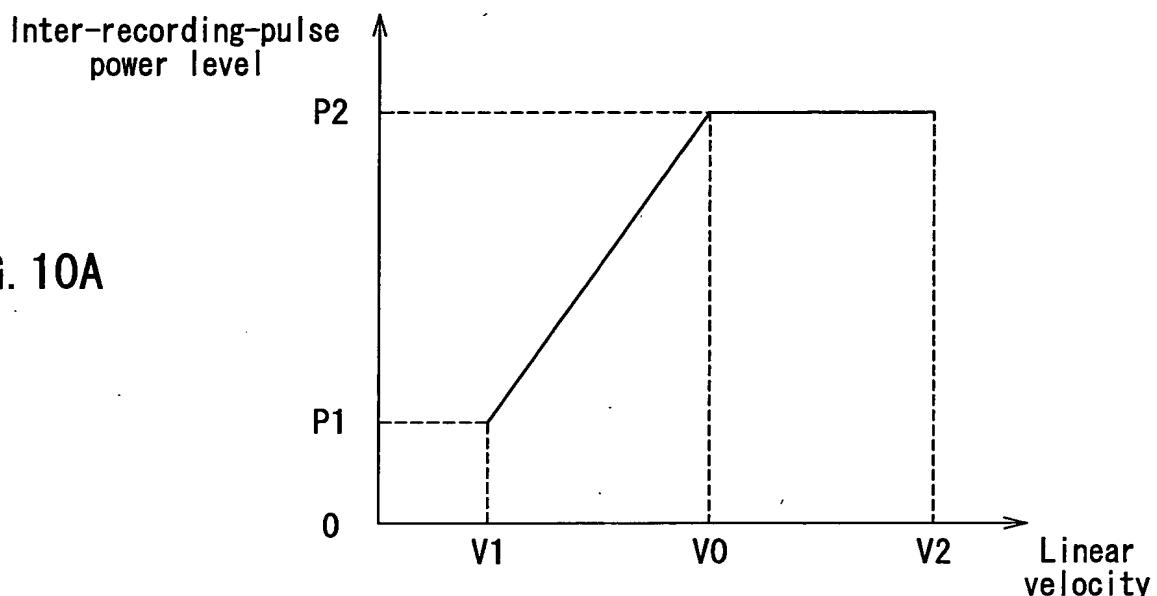
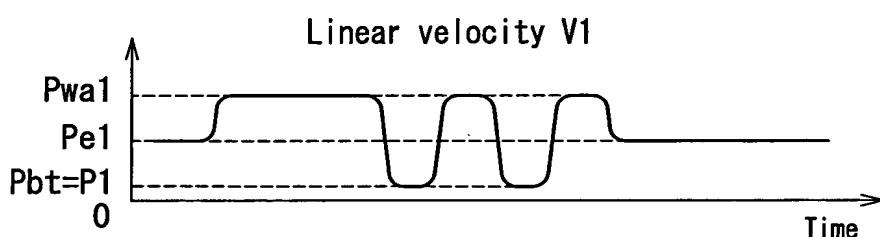
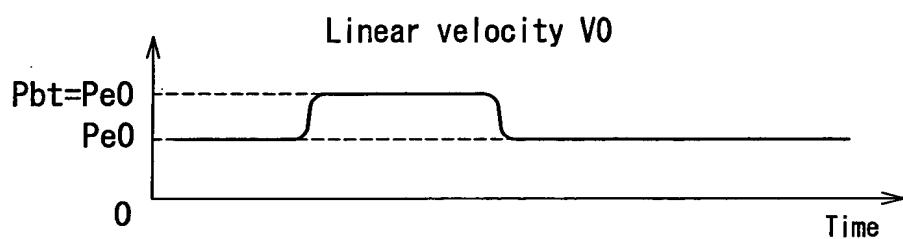
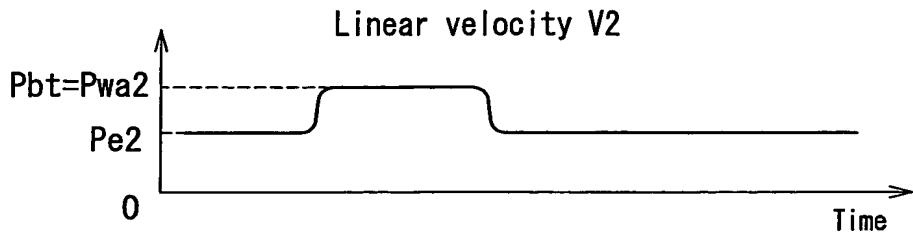
FIG. 8C



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**FIG. 9B****FIG. 9C****FIG. 9D**

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**FIG. 10B****FIG. 10C****FIG. 10D**

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FIG. 11A

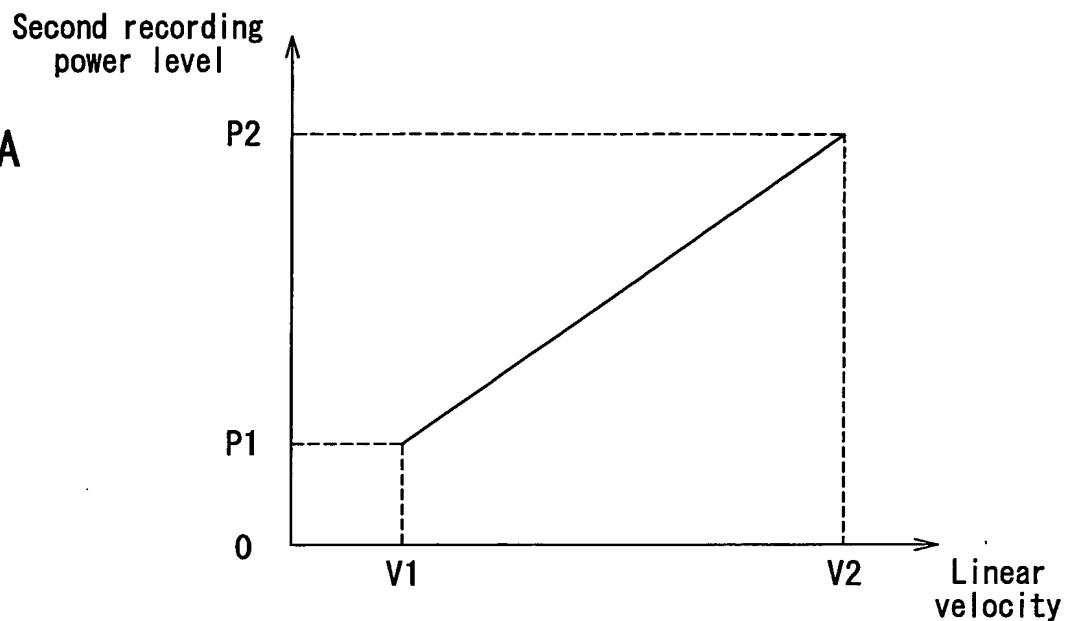


FIG. 11B

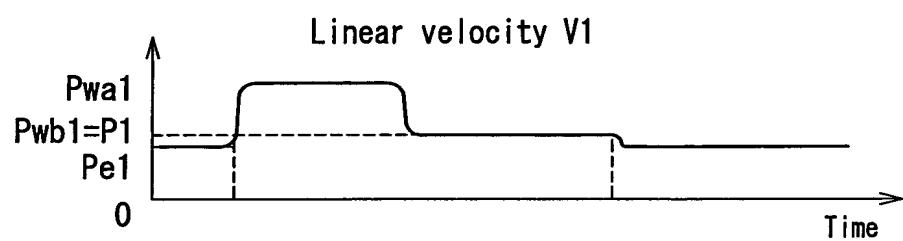
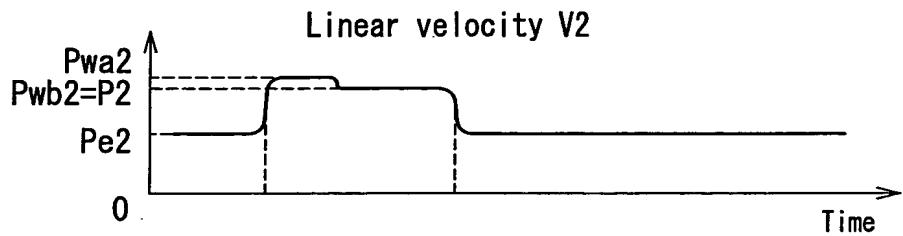


FIG. 11C



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Inter-recording-pulse power level

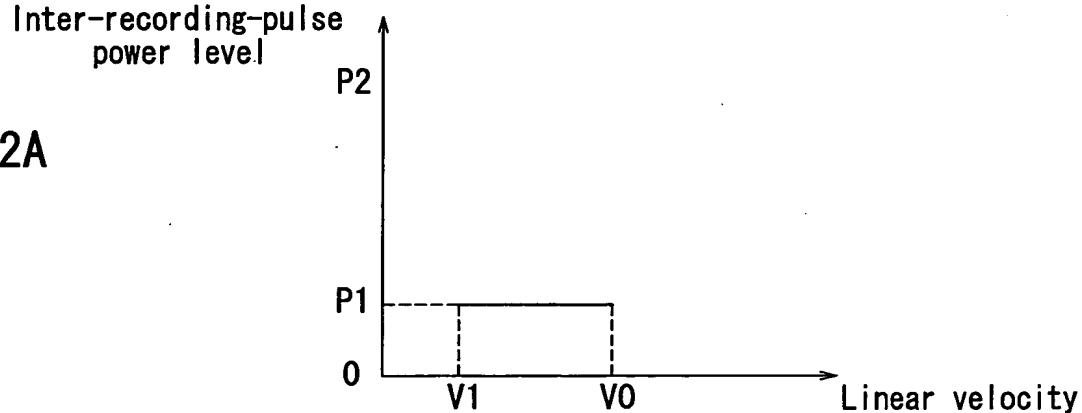


FIG. 12A

Second recording power level

FIG. 12B

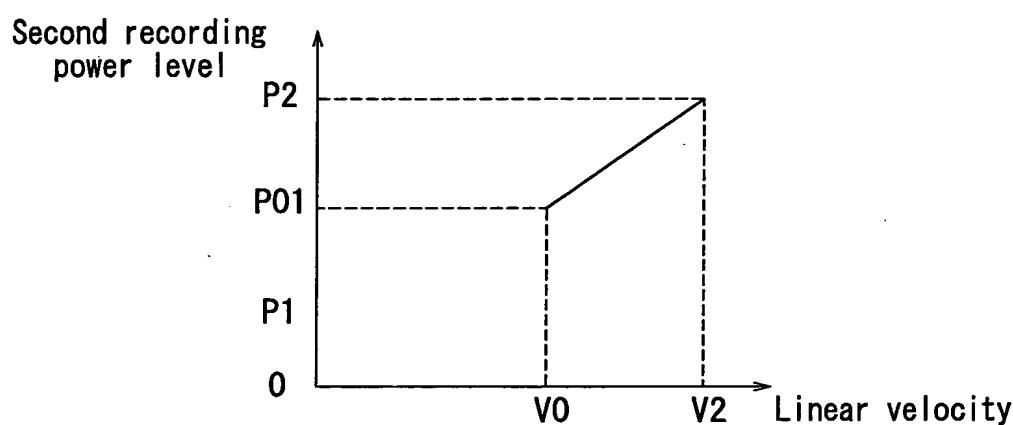


FIG. 12C

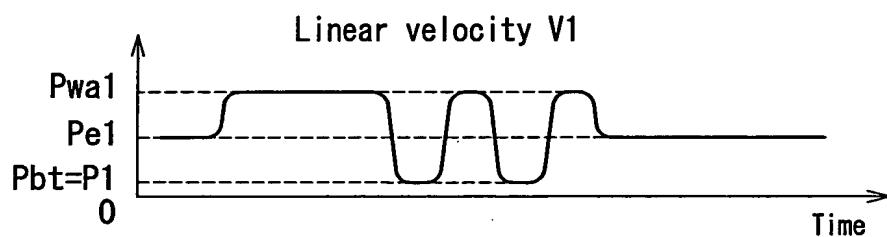


FIG. 12D

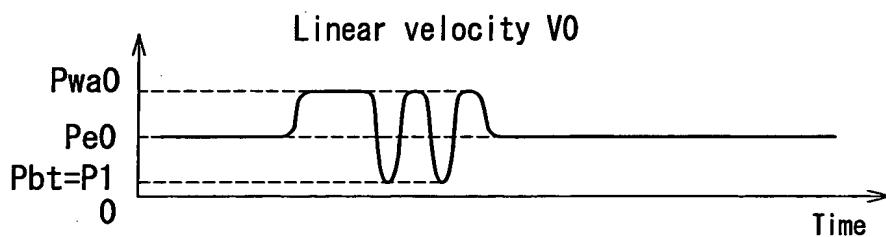
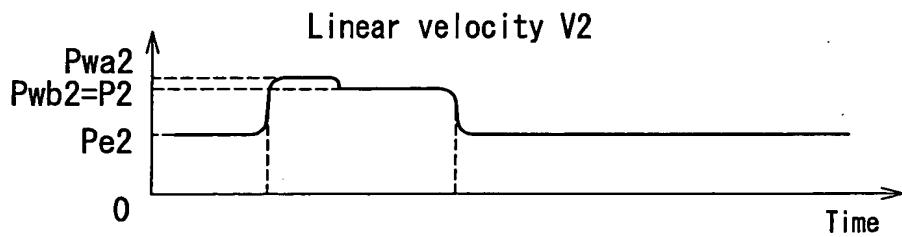


FIG. 12E



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FIG. 13A

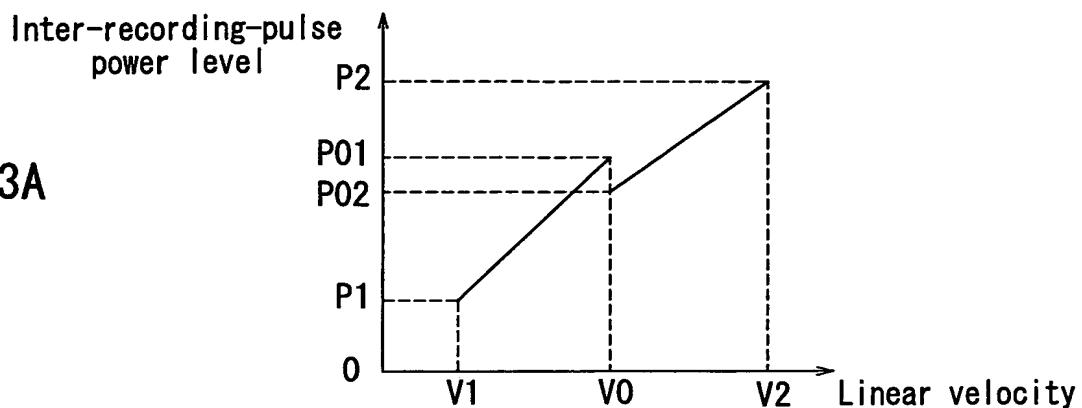


FIG. 13B

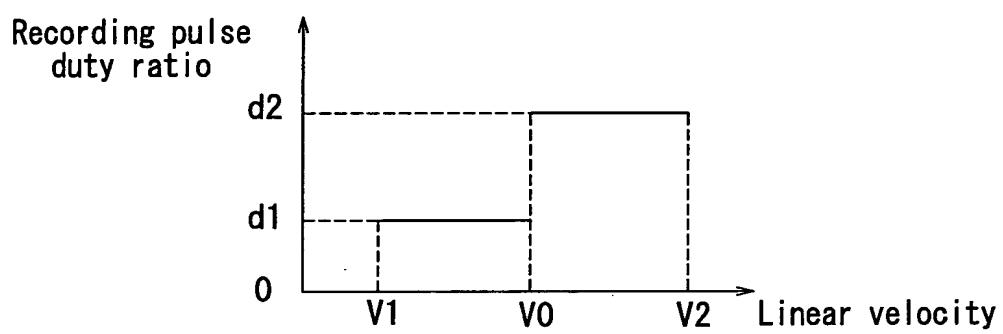


FIG. 13C

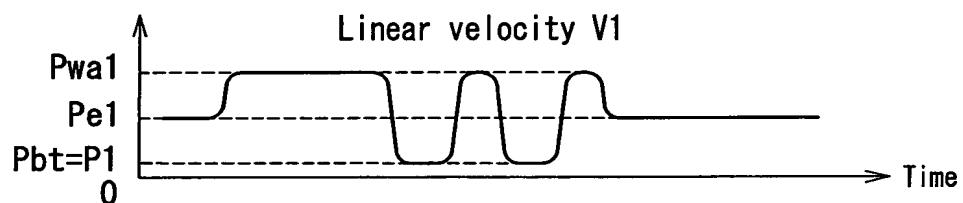


FIG. 13D

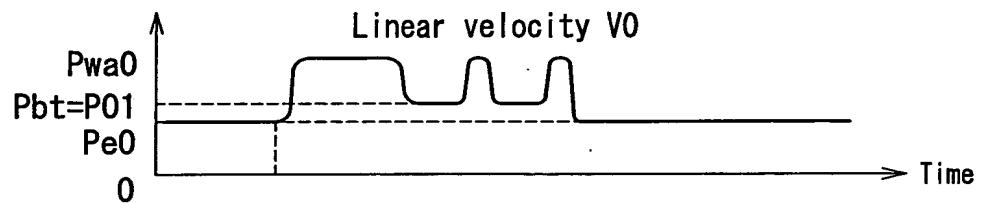


FIG. 13E

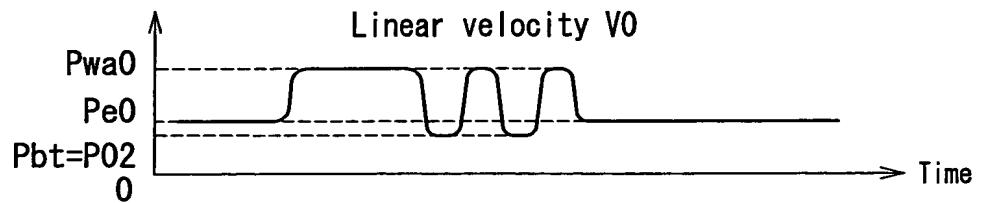
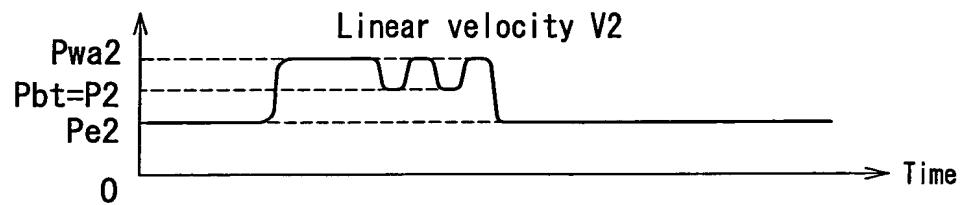
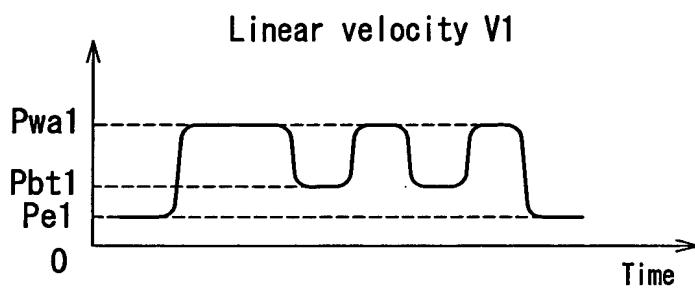
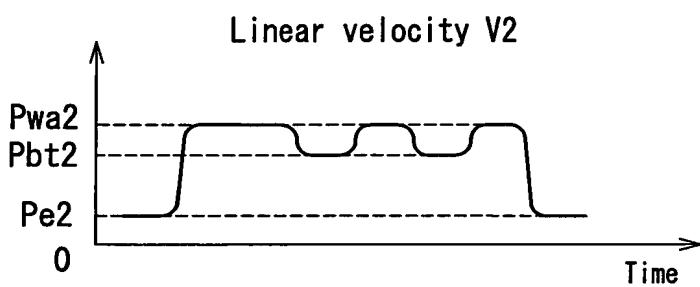
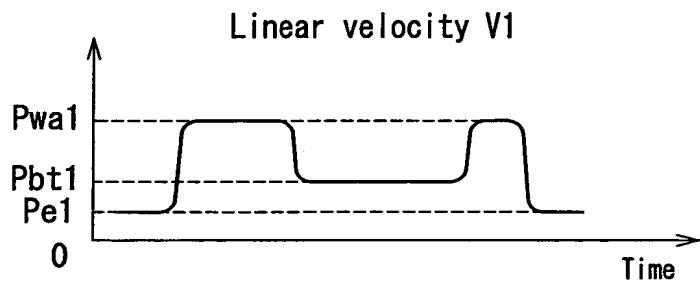
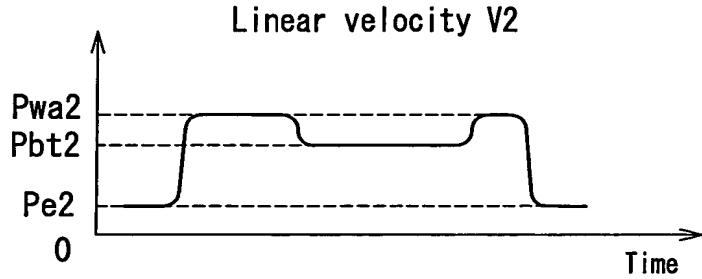


FIG. 13F



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FIG. 14A**FIG. 14B****FIG. 15A****FIG. 15B**

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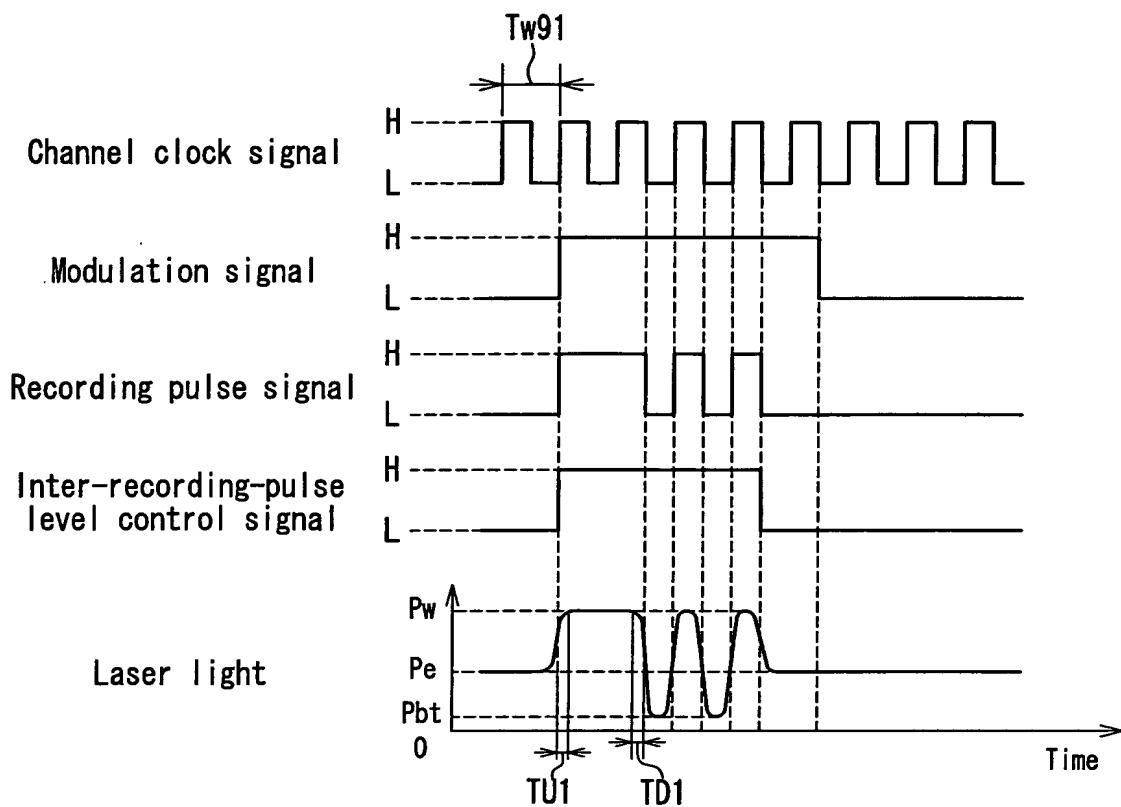


FIG. 16

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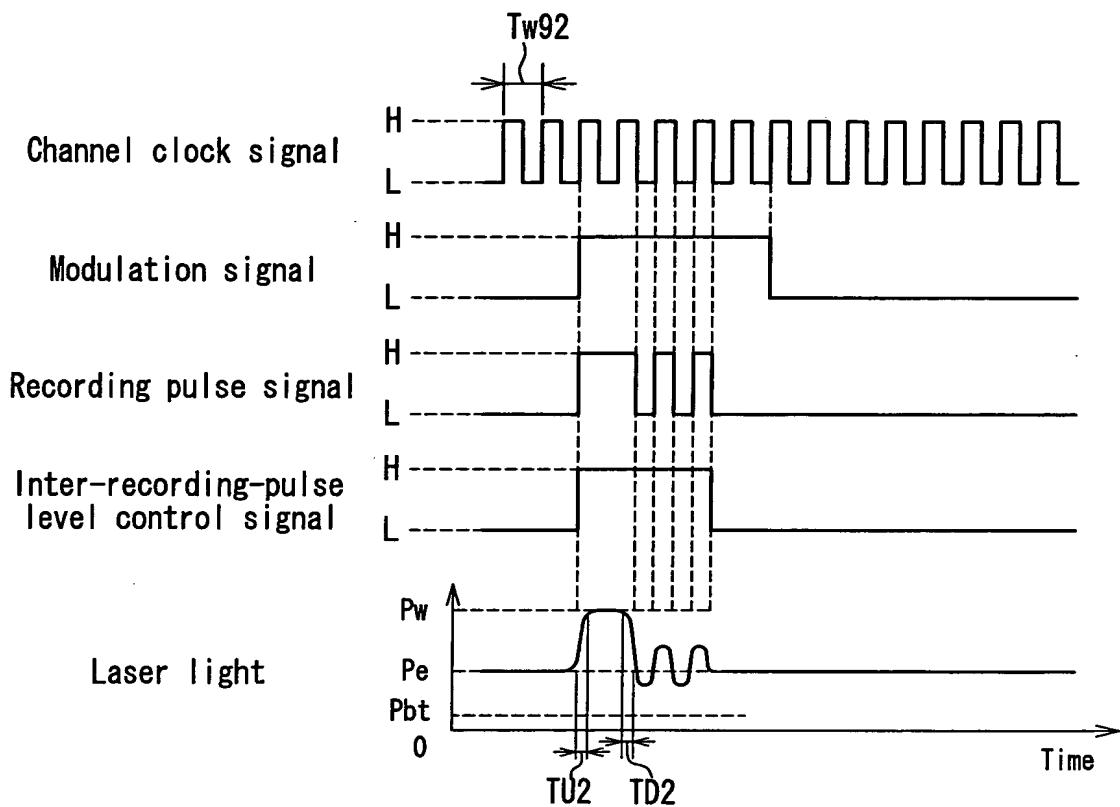


FIG. 17

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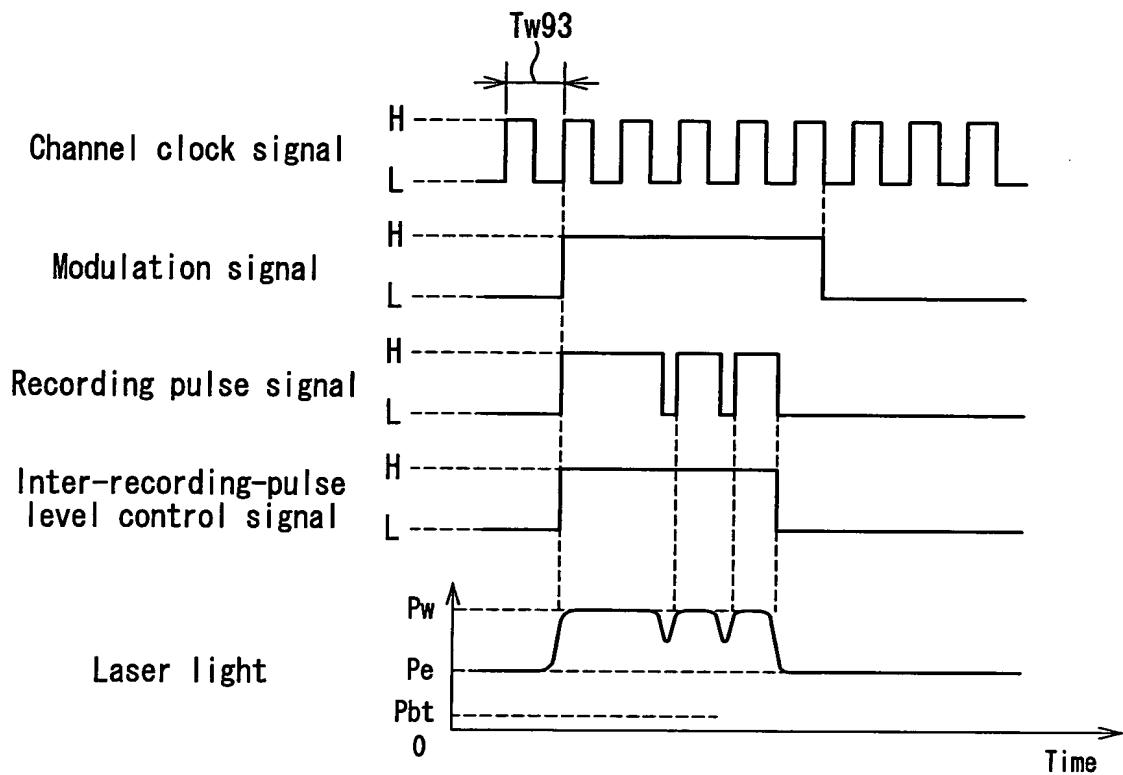


FIG. 18

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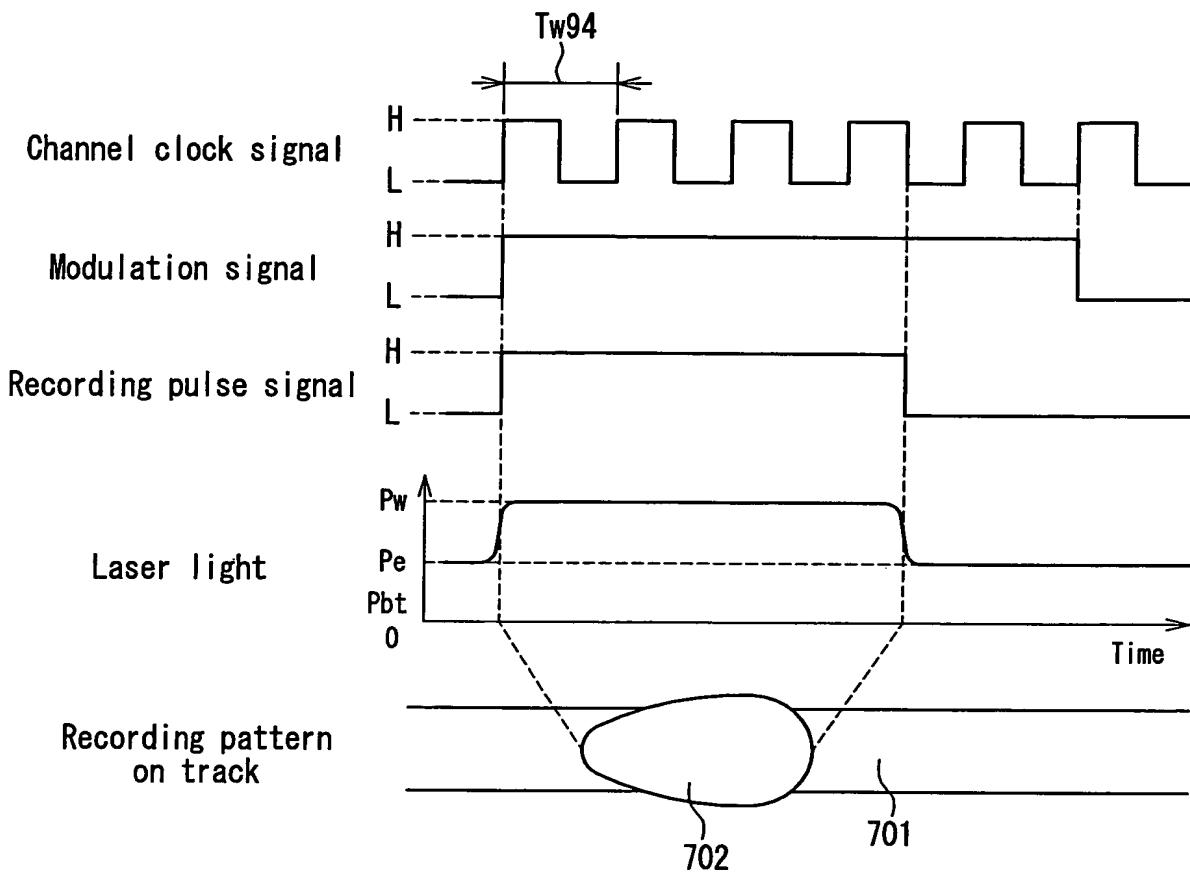


FIG. 19